

Substitute for form 1449A/PTO (modified)

Application Number

10/076174

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Filing Date

February 14, 2002

First Named Inventor

Baude, Paul F.

Art Unit

4756 2823

Examiner Name

M. Estrada

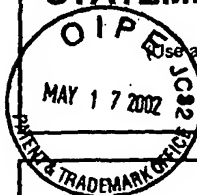
Attorney Case Number

57322US002

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**U.S. Patent Documents**

Exam. Init.*	Cite No.	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Doc. Number-(Kind Code if Known)			
ONE	A1	US- 3,669,060	06/13/72	Page et al.	
ONE	A2	US- 4,096,821	06/27/78	Greeneich et al.	
ONE	A3	US- 4,335,161	06/15/82	Luo	
ONE	A4	US- 4,676,193	06/30/87	Martin	
ONE	A5	US- 4,746,548	05/24/88	Boudreau et al.	
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ONE	A7	US- 4,963,921	10/16/90	Kariya et al.	
ONE	A8	US- 5,397,607	03/14/95	Shields et al.	
ONE	A9	US- 6,087,196	07/11/00	Sturm et al.	
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ONE	A11	US- 6,285,001 B1	09/04/01	Fleming et al.	

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**Foreign Patent Documents**

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ONE	B1	EP	0 118 576	12/02/87			
ONE	B2	EP	0719 638 A2	07/03/96			
ONE	B3	JP	5-228669	09/07/93			X
ONE	B4	JP	9-27454	01/28/97			X
ONE	B5	PCT	WO 99/30336	06/17/99			
ONE	B6	PCT	WO 99/39373	08/05/99			
ONE	B7	PCT	WO 99/54786	10/28/99			

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Exam. Init.*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
ONE	C1	Brody, "Large Area Masking Techniques for Thin Film Transistor Arrays," SPIE, Vol. 100, Semiconductor Microlithography II, pp. 140-150(1977)
ONE	C2	Weimer, "The TFT- A New Thin-Film Transistor," Proceedings of the IRE, pp. 1462-1469(1962).
ONE	C3	Brody et al., "Flexible Thin-Film Transistors Stretch Performance; Shrink Cost," Research, pp. 20-23 (1969).

\*Examiner:

Michelle Estrada

Date Considered:

10/28/02

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement - PTO-1449 (Modified)

Substitut for form 1449A/PTO (modified)  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)  MAY 17 2002 Page 2 of 2	Applicati n Numb r	10/076174
	Filing Dat	February 14, 2002
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	Examiner Name	M. Estrada
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ME	C4	Schon et al., "Fast Organic Electronic Circuits Based on Ambipolar Pentacene Field-Effect Transistors," Applied Physics Letters, Vol. 79, No. 24, pp. 4043-4044 (2001),
ME	C5	Gouy et al., "Deposition of Organic Semiconductor Through Silicon Shadow Masks for Integrated Led on Silicon."
ME	C6	Kim et al., "Photoplastic Shadow-Masks for Rapid Resistless Multi-Layer Micropatterning".
ME	C7	Kim et al., "Dry-Lift-Off Patterning on Fragile Surfaces Using MEMS Shadow Masks," pp. 619-622.

*Examiner: <i>Michelle Estrada</i>	Dat Considered: <i>10/28/03</i>
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First Named Inventor	Baude, Paul F.
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Examiner Name	Unknown M. Estrada
Attorney Case Number	57322US002

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	A1	US-			
	A2	US-			
	A3	US-			
	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
	A10	US-			
	A11	US-			

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		Ctry. Code	Number-KindCode (if known)				
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	B2						
	B3						
	B4						
	B5						
	B6						
	B7						

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	C3	

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Information Disclosure Statement — PTO-1449 (Modified)

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me	A1	US- 2002/0062785A1	05/30/2002	Kim et al.	RECEIVED DEC 23 2002 TC 2800 MAIL ROOM
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	A8	US-			
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	A10	US-			
	A11	US-			

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ME	A1	US- 5,626,784	05/06/1997	Simons	
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	A3	US-			
	A4	US-			
	A5	US-			
	A6	US-			
	A7	US-			
	A8	US-			
	A9	US-			
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ME	B2	GB	2 276 589 A	10/05/1994	David Godfrey Williams		
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